N-Channel Power MOSFET 600 V, 1.2 Ω

Features

- Low ON Resistance
- Low Gate Charge
- ESD Diode-Protected Gate
- 100% Avalanche Tested
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^{\circ}C$ unless otherwise noted)

Rating	Symbol	NDF	NDP	Unit
Drain-to-Source Voltage	V_{DSS}	600		٧
Continuous Drain Current, R _{θJC} (Note 1)	I _D	7.	7.1	
Continuous Drain Current T _A = 100°C, R _{θJC} (Note 1)	I _D	4.	5	Α
Pulsed Drain Current, V _{GS} @ 10 V	I _{DM}	2	8	Α
Power Dissipation, R _{θJC}	P_{D}	35	156	W
Gate-to-Source Voltage	V_{GS}	±30		V
Single Pulse Avalanche Energy, L = 6.3 mH, I_D = 6.0 A	E _{AS}	113		mJ
ESD (HBM) (JESD22-A114)	V _{esd}	3000		V
RMS Isolation Voltage (t = 0.3 sec., R.H. ≤ 30%, T _A = 25°C) (Figure 13)	V _{ISO}	4500 –		V
Peak Diode Recovery (Note 2)	dv/dt	4.5		V/ns
Continuous Source Current (Body Diode)	I _S	6.0		Α
Maximum Temperature for Soldering Leads	TL	260		°C
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to 150		°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

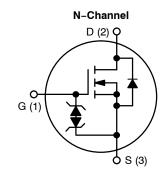
- 1. Limited by maximum junction temperature
- 2. $I_{SD} = 6.0$ Å, $di/dt \le 100$ Å/ μ s, $V_{DD} \le BV_{DSS}$, $T_J = +150$ °C

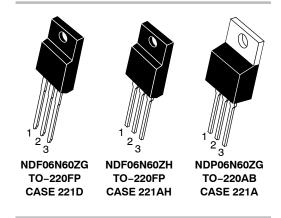


ON Semiconductor®

http://onsemi.com

V _{DSS}	R _{DS(ON)} (MAX) @ 3 A
600 V	1.2 Ω





ORDERING AND MARKING INFORMATION

See detailed ordering, marking and shipping information in the package dimensions section on page 6 of this data sheet.

THERMAL RESISTANCE

Parameter	Symbol	NDF06N60Z	NDP06N60Z	Unit
Junction-to-Case (Drain)	$R_{\theta JC}$	3.6	0.8	°C/W
Junction-to-Ambient Steady State (Note 3)	$R_{\theta JA}$	50	50	

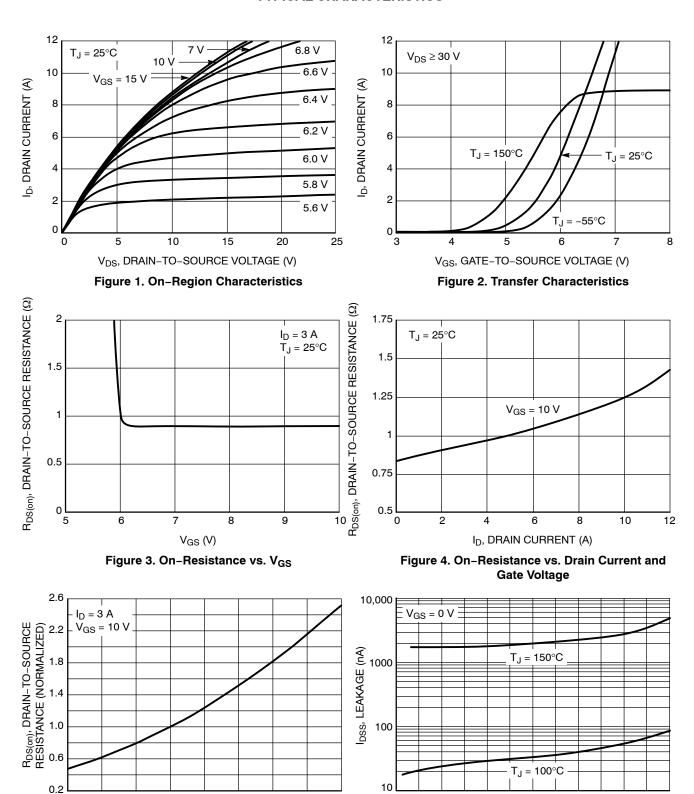
^{3.} Insertion mounted

ELECTRICAL CHARACTERISTICS (T_{.1} = 25°C unless otherwise noted)

Characteristic	Test Conditions		Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					•	•	
Drain-to-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}$	1	BV _{DSS}	600			V
Breakdown Voltage Temperature Coefficient	Reference to 25°C, $I_D = 1 \text{ mA}$		$\Delta BV_{DSS}/ \Delta T_{J}$		0.6		V/°C
Drain-to-Source Leakage Current	.,	25°C	I _{DSS}			1	μΑ
	$V_{DS} = 600 \text{ V}, V_{GS} = 0 \text{ V}$	150°C	1			50	
Gate-to-Source Forward Leakage	V _{GS} = ±20 V	•	I _{GSS}			±10	μΑ
ON CHARACTERISTICS (Note 4)					•	•	
Static Drain-to-Source On-Resistance	$V_{GS} = 10 \text{ V}, I_D = 3.0 \text{ A}$	A	R _{DS(on)}		0.98	1.2	Ω
Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 100 μ	A	V _{GS(th)}	3.0		4.5	V
Forward Transconductance	V _{DS} = 15 V, I _D = 3.0 A		9FS		5.0		S
DYNAMIC CHARACTERISTICS							
Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz		C _{iss}		923		pF
Output Capacitance			C _{oss}		106		
Reverse Transfer Capacitance			C _{rss}		23		
Total Gate Charge	V _{DD} = 300 V, I _D = 6.0 A, V _{GS} = 10 V		Q_g		31		nC
Gate-to-Source Charge			Q _{gs}		6.3		1
Gate-to-Drain ("Miller") Charge			Q _{gd}		17		
Gate Resistance			R_{g}		3.2		Ω
RESISTIVE SWITCHING CHARACTERI	STICS						
Turn-On Delay Time	V_{DD} = 300 V, I_{D} = 6.0 A, V_{GS} = 10 V, R_{G} = 5 Ω		t _{d(on)}		13		ns
Rise Time			t _r		17		
Turn-Off Delay Time			t _{d(off)}		30		
Fall Time			t _f		28		
SOURCE-DRAIN DIODE CHARACTER	ISTICS (T _C = 25°C unless oth	erwise not	ed)				
Diode Forward Voltage	I _S = 6.0 A, V _{GS} = 0 \	′	V_{SD}			1.6	V
Reverse Recovery Time	V _{GS} = 0 V, V _{DD} = 30 V		t _{rr}		338		ns
Reverse Recovery Charge	I _S = 6.0 A, di/dt = 100 A/μs		Q _{rr}		2.0		μС

^{4.} Pulse Width \leq 380 μ s, Duty Cycle \leq 2%.

TYPICAL CHARACTERISTICS



T_J, JUNCTION TEMPERATURE (°C)

Figure 5. On–Resistance Variation with
Temperature

-50

-25

Figure 6. Drain-to-Source Leakage Current vs. Voltage

V_{DS}, DRAIN-TO-SOURCE VOLTAGE (V)

TYPICAL CHARACTERISTICS

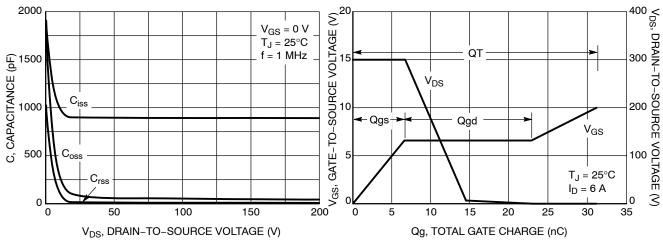


Figure 7. Capacitance Variation

Figure 8. Gate-to-Source and Drain-to-Source Voltage vs. Total Charge

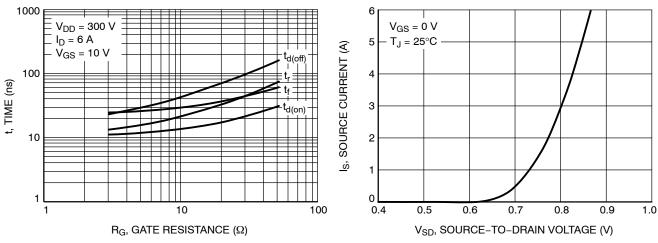


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

Figure 10. Diode Forward Voltage vs. Current

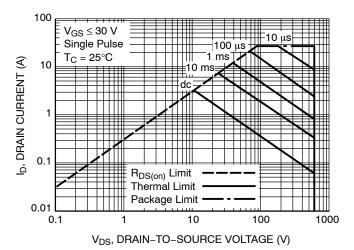


Figure 11. Maximum Rated Forward Biased Safe Operating Area for NDF06N60Z

TYPICAL CHARACTERISTICS

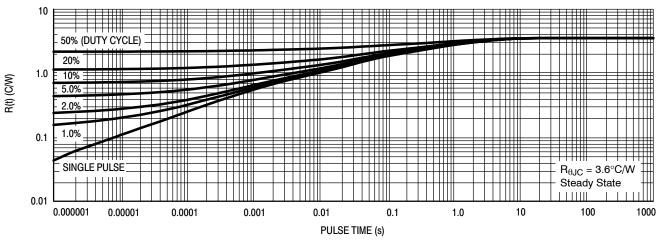


Figure 12. Thermal Impedance for NDF06N60Z

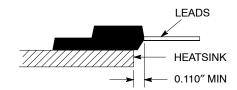


Figure 13. Mounting Position for Isolation Test

Measurement made between leads and heatsink with all leads shorted together.

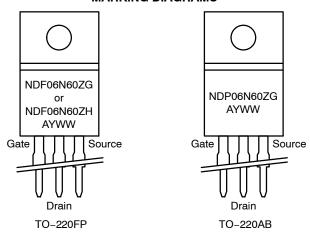
^{*}For additional mounting information, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ORDERING INFORMATION

Order Number	Package	Shipping [†]
NDF06N60ZG	TO-220FP (Pb-Free, Halogen-Free)	50 Units / Rail
NDF06N60ZH	TO-220FP (Halogen-Free)	50 Units / Rail
NDP06N60ZG	TO-220AB (Pb-Free)	50 Units / Rail (In Development)

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MARKING DIAGRAMS



A = Location Code

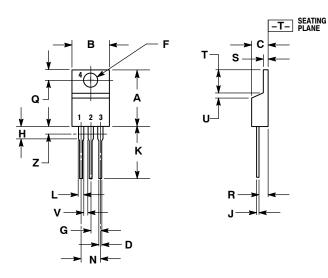
Y = Year

WW = Work Week

G, H = Pb-Free, Halogen-Free Package

PACKAGE DIMENSIONS

TO-220AB CASE 221A-09 **ISSUE AF**



NOTES:

- NOTES:

 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

 2. CONTROLLING DIMENSION: INCH.

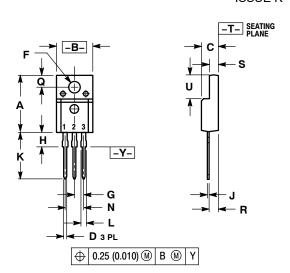
 3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.161	3.61	4.09
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.014	0.025	0.36	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
Т	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

- STYLE 5: PIN 1. GATE
 - 2. DRAIN
 - SOURCE DRAIN

TO-220 FULLPAK

CASE 221D-03 ISSUE K



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH
 3. 221D-01 THRU 221D-02 OBSOLETE, NEW STANDARD 221D-03.

	INCHES		MILLIN	IETERS		
DIM	MIN	MAX	MIN	MAX		
Α	0.617	0.635	15.67	16.12		
В	0.392	0.419	9.96	10.63		
С	0.177	0.193	4.50	4.90		
D	0.024	0.039	0.60	1.00		
F	0.116	0.129	2.95	3.28		
G	0.100	0.100 BSC		2.54 BSC		
Н	0.118	0.135	3.00	3.43		
J	0.018	0.025	0.45	0.63		
K	0.503	0.541	12.78	13.73		
L	0.048	0.058	1.23	1.47		
N	0.200	BSC	5.08	BSC		
Q	0.122	0.138	3.10	3.50		
R	0.099	0.117	2.51	2.96		
S	0.092	0.113	2.34	2.87		
U	0.239	0.271	6.06	6.88		

STYLE 1: PIN 1. GATE

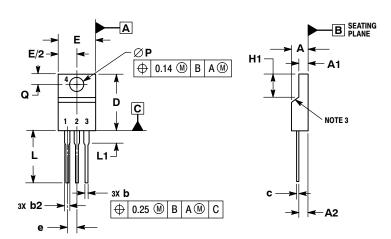
2. 3.

DRAIN SOURCE

PACKAGE DIMENSIONS

TO-220 FULLPAK, 3-LEAD

CASE 221AH-01 ISSUF O



NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. CONTOUR UNCONTROLLED IN THIS AREA.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH AND GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE TO BE MEASURED AT OUTERMOST EXTREME OF THE PLASTIC BODY.
- 5. DIMENSION b2 DOES NOT INCLUDE DAMBAR PROTRUSION. LEAD WIDTH INCLUDING PROTRUSION SHALL NOT EXCEED 2.00.

	MILLIMETERS			
DIM	MIN	MAX		
Α	4.30	4.70		
A1	2.50	2.90		
A2	2.50	2.70		
b	0.54	0.84		
b2	1.10	1.40		
С	0.49	0.79		
D	14.22	15.88		
Ε	9.65	10.67		
е	2.54	2.54 BSC		
H1	5.97	6.48		
L	12.70	14.73		
L1		2.80		
Р	3.00	3.40		
Q	2.80	3.20		

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